

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 6,308,317
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It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

IN THE CLAIMS OF THE REEXAMINATION CERTIFICATE:

58. A microcontroller comprising:

. . . . renumbering byte codes in the compiled form to equivalent byte codes in an instruction set supported by an interpreter on the [integrated circuit card] microcontroller

65. A microcontroller having a set of resource constraints and comprising:

. . . . renumbering byte codes in the compiled form to equivalent byte codes in an instruction set supported by an interpreter on the [integrated circuit card] microcontroller

78. A method of programming a microcontroller having a memory and a processor operating according to a set of resource constraints, the method comprising the steps of:

. . . . renumbering byte codes in the compiled form to equivalent byte codes in an instruction set supported by an interpreter on the [integrated circuit card] microcontroller

87. A microcontroller comprising:

. . . . renumbering byte codes in the compiled form to equivalent byte codes in an instruction set supported by an interpreter on the [integrated circuit card] microcontroller

88. A method of programming a microcontroller having a memory and a processor operating according to a set of resource constraints, the method comprising the steps of:

. . . . renumbering byte codes in the compiled form to equivalent byte codes in an instruction set supported by an interpreter on the [integrated circuit card] microcontroller

92. A method of programming a microcontroller having a memory and a processor operating according to a set of resource constraints, the method comprising the steps of:

. . . . renumbering byte codes in the compiled form to equivalent byte codes in an instruction set supported by an interpreter on the [integrated circuit card] microcontroller

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